



SANJIVANI RURAL EDUCATION SOCIETY'S
SANJIVANI COLLEGE OF ENGINEERING KOPARGAON
(An Autonomous Institute Affiliated to Savitribai Phule Pune University, Pune)



**DEPARTMENT OF ELECTRONICS AND TELECOMMUNICATION
ENGINEERING**

M. Tech. Electronics Engineering (Digital Systems)

Program Curriculum Structure

(2021 Pattern)

(M. Tech. First year w. e. f. academic year 2021-2022)

(Dr. B. S. Agarkar)
HOD and Chairman BoS
E&TC

(Dr. A. B. Pawar)
Dean Academics

(Dr. A. G. Thakur)
Director and Chairman
Academic Council

SRES's Sanjivani College of Engineering, Kopargaon
(An Autonomous Institute)
Teaching and Evaluation Scheme
M. Tech. Electronics Engineering(Digital Systems)

List of Abbreviations			
Abbreviation	Full Form	Abbreviation	Full Form
PCC	Professional Core Courses	ISE	In-Semester Evaluation
PEC	Professional Elective Courses	ESE	End-Semester Evaluation
OEC	Open Elective Courses	OR	End-Semester Oral Examination
L	Lecture	PR	End-Semester Practical Examination
T	Tutorial	TW	Continuous Term Work Evaluation
P	Practical	PROJ	Project
LC	Laboratory Courses	Cat.	Category
CA	Continuous Assessment	MLC	Mandatory Course
AC	Audit Course		

First Year (Semester I)

Course			Teaching Scheme (Hrs/Week)				Evaluation Scheme-Marks				
Cat.	Code	Title	L	T	P	Credits	Theory		OR	TW	Marks
							ESE	CIA			
PCC	ET601	CMOS Circuit Design	3	-	-	3	60	40	-	-	100
PCC	ET602	Advanced Embedded System Design	3	-	-	3	60	40	-	-	100
PEC	ET603	Elective 1 -Advanced Engineering Mathematics -Multi-rate and Adaptive Signal Processing -Advanced Communication Engineering	3	-	-	3	60	40	-	-	100
PEC	ET604	Elective 2 -Random Signal Processing -Image Processing and Pattern Recognition -Network Routing	3	-	-	3	60	40	-	-	100
LC	ET605	Lab practice-I (Digital Design Lab 1)	-	-	4	2	-	-	50	-	50
LC	ET606	Lab practice-II (Elective Lab 1)	-	-	4	2	-	-	50	-	50
MLC	ET607	Research Methodology & IPR	2	-	-	2	50	-	-	-	50
AC	AC608	Audit Course 1	2	-	-	-	-	-	-	-	-
Total			16	0	8	18	290	160	100	-	550

*Oral will be based on the term work submitted by the student.

(Dr. B. S. Agarkar)
HOD and Chairman BoS
E&TC

(Dr. A. B. Pawar)
Dean Academics

(Dr. A. G. Thakur)
Director and Chairman
Academic Council

First Year (Semester II)

Course			Teaching Scheme (Hrs/Week)				Evaluation Scheme-Marks				
Cat.	Code	Title	L	T	P	Credits	Theory		OR	TW	Marks
							ESE	CIA			
PCC	ET609	Embedded OS and Software Tools	3	-	-	3	60	40	-	-	100
PCC	ET610	Advanced DSP	3	-	-	3	60	40	-	-	100
PEC	ET611	Elective 3 -PLDs and ASIC Design -Reconfigurable Computing -Design for Testability	3	-	-	3	60	40	-	-	100
PEC	ET612	Elective 4 -Soft Computing -Speech and Audio Processing -Internet of Things for Smart Infrastructure	3	-	-	3	60	40	-	-	100
LC	ET613	Lab practice-III (Digital Design Lab 2)	-	-	4	2	-	-	50*	-	50
LC	ET614	Lab practice-IV (Elective Lab 2)	-	-	4	2	-	-	50*	-	50
PROJ	ET615	Mini Project	-	-	4	2	-	-	50*	-	50
AC	AC616	Audit Course 2	2	-	-	-	-	-	-	-	-
Total			14	-	12	18	240	160	150	-	550

*Oral will be based on the term work submitted by the student.

Audit course 1 & 2

1. English for Research Paper Writing
2. Disaster Management
3. Sanskrit for Technical Knowledge
4. Value Education
5. Constitution of India
6. Pedagogy Studies
7. Stress Management by Yoga
8. Personality Development through Life Enlightenment Skills.

(Dr. B. S. Agarkar)
HOD and Chairman BoS
E&TC

(Dr. A. B. Pawar)
Dean Academics

(Dr. A. G. Thakur)
Director and Chairman
Academic Council

SRES's Sanjivani College of Engineering, Kopargaon
(An Autonomous Institute)
Teaching and Evaluation Scheme
M. Tech. Electronics Engineering (Digital Systems)
Second Year (Semester III)

Course			Teaching Scheme (Hrs/Week)				Evaluation Scheme-Marks				
Cat	Code	Title	L	T	P	Credits	Theory		OR	TW	Marks
							ESE	CIA			
PEC	ET701	Elective 5 -Embedded Automotive Systems -MEMS and Nano-devices -Design of Wireless Sensor Networks	3	-	-	3	60	40	-		100
OEC	CE702 ET702 MB702 ME702 ST702 CO702	Open Elective -Industrial safety and management -Machine Learning -Start up and Venture management -Project Planning and Operation Research -Composite Materials -Recent Trends in Computer Technology	3	-	-	3	60	40	-	-	100
PROJ	ET703	Dissertation Phase 1	-	-	20	10	-	-	50*	-	50
Total			6	-	20	16	120	80	50	-	250

*Oral will be based on the term work submitted by the student.

Second Year (Semester IV)

Course			Teaching Scheme (Hrs/Week)				Evaluation Scheme-Marks				
Cat	Code	Title	L	T	P	Credits	Theory		OR	TW	Marks
							ESE	CIA			
PROJ	ET704	Dissertation Phase 2	-	-	32	16	-	-	100	50	150
Total			-	-	32	16	-	-	100	50	150

(Dr. B. S. Agarkar)
HOD and Chairman BoS
E&TC

(Dr. A. B. Pawar)
Dean Academics

(Dr. A. G. Thakur)
Director and Chairman
Academic Council